

Using SSI (Signal Strength Indicator) to monitor Ionosphere

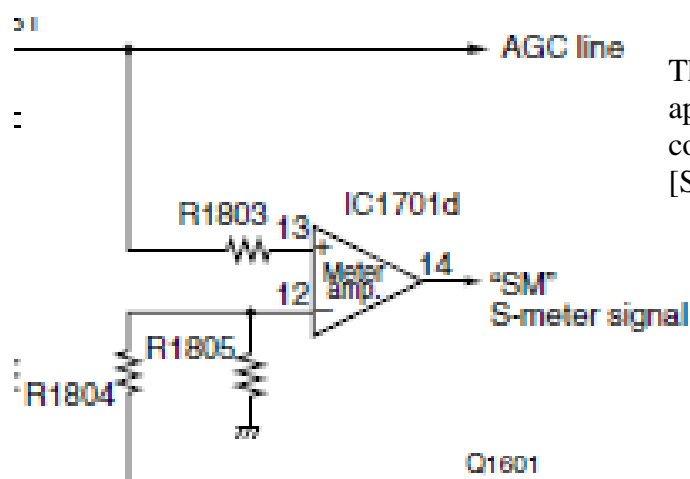
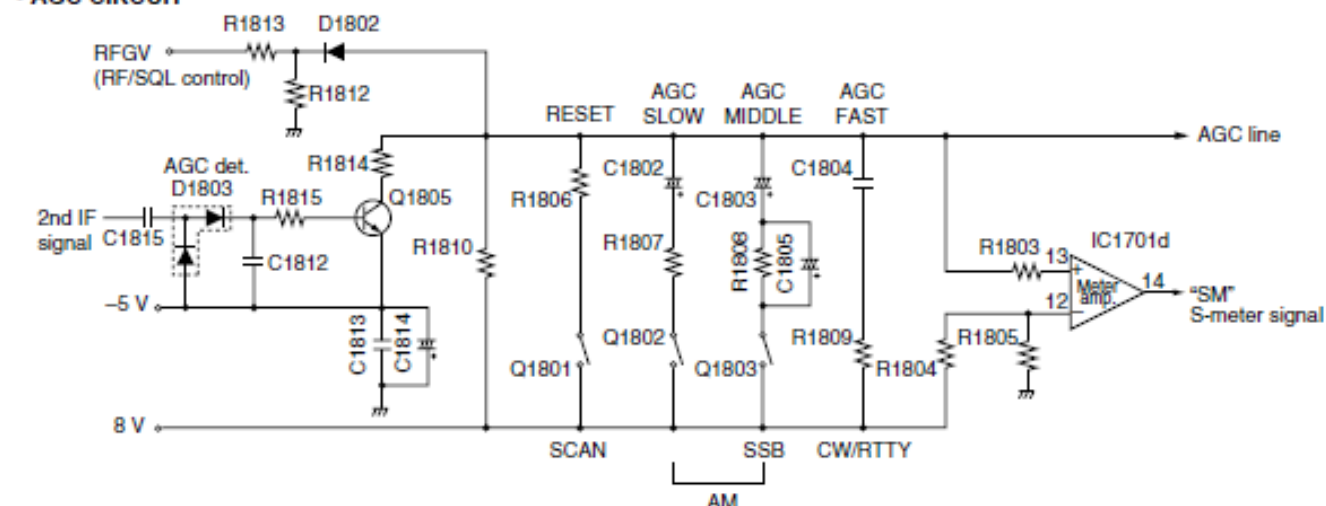
Icom IC-718 Service Manual Addendum.pdf

January 2013

4-1-14 S-METER CIRCUIT (MAIN UNIT)

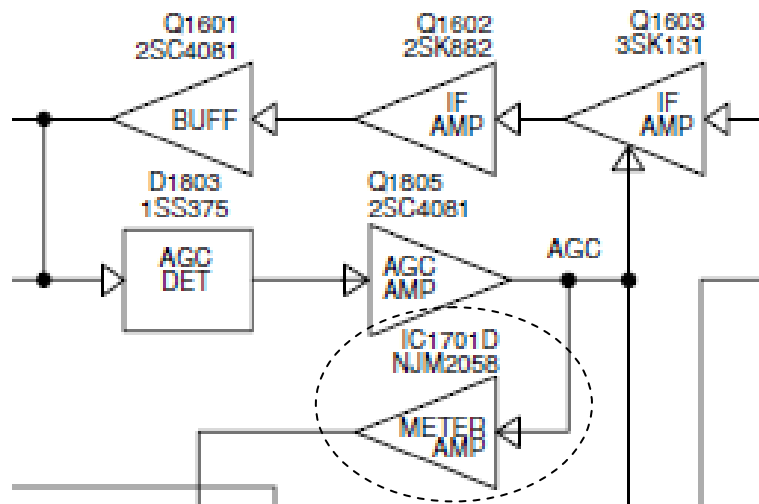
The S-meter circuit indicates the relative received signal strength while receiving by utilizing the AGC voltage which changes depending on the received signal strength. The AGC bias voltage (time constant line) is applied to a differential amplifier (IC1701d, pin 13) where the difference between the bias and reference voltage is detected. The S-meter signal is applied to the A/D converter section in the CPU (LOGIC unit; IC1, pin 98) and the S/R/F indicator displays the relative signal strength.

• AGC CIRCUIT

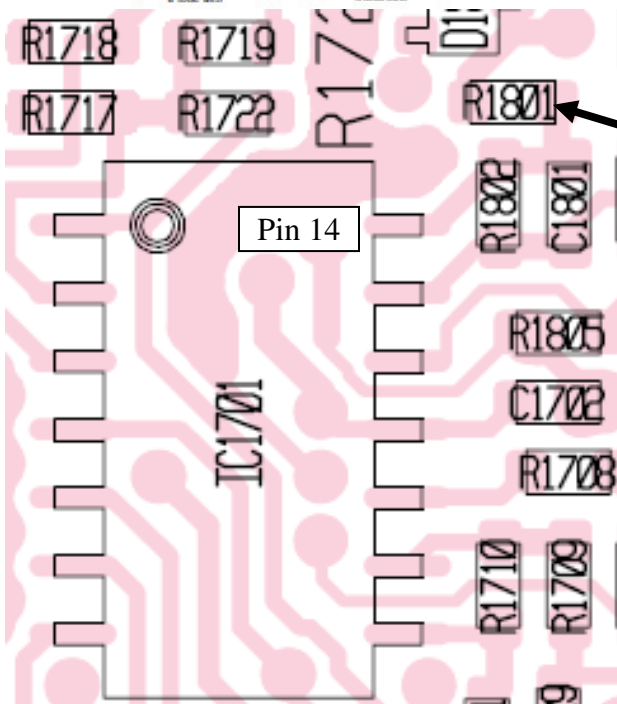
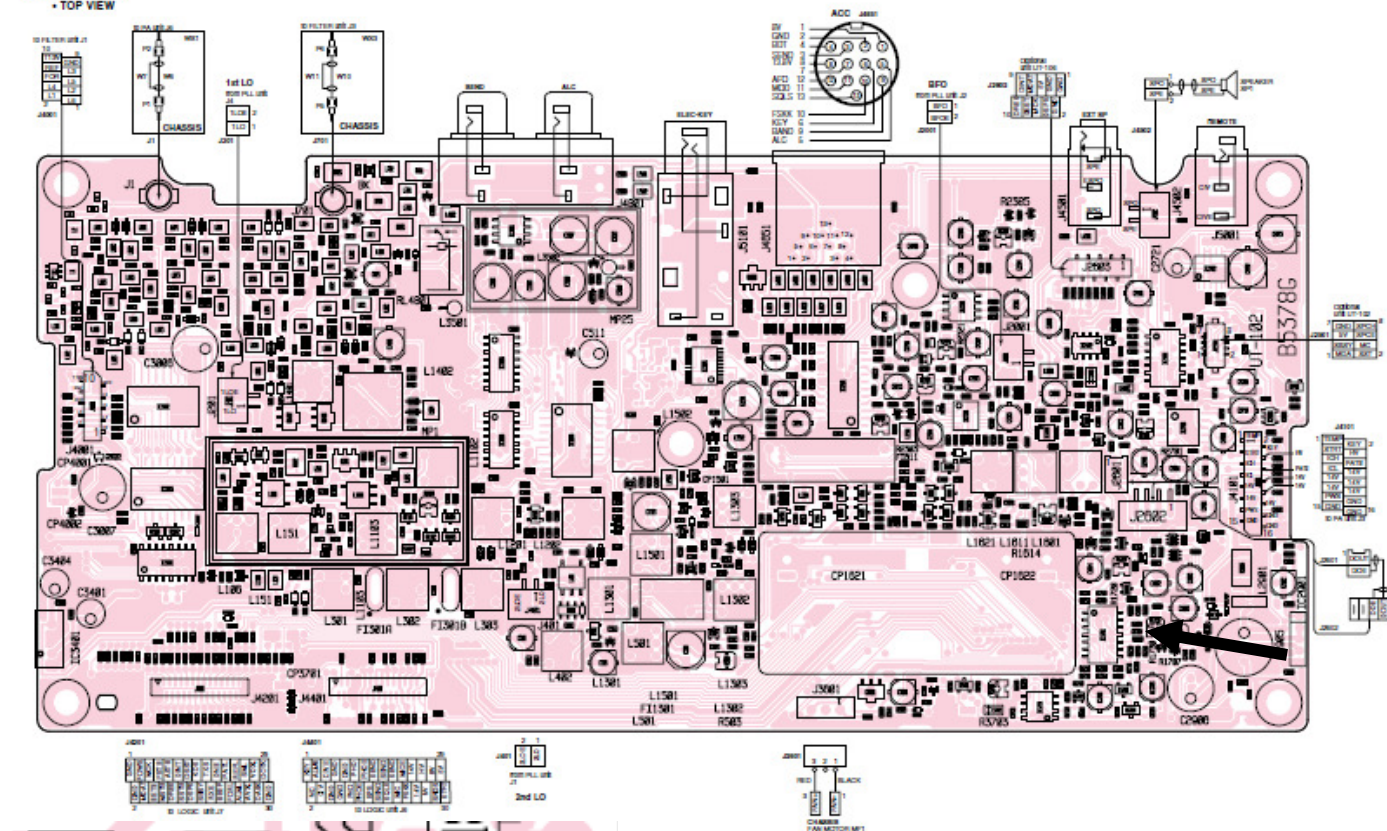


The S-meter signal from IC1701d (pin 14) is applied to the CPU (LOGIC unit; IC1) to be compared with the threshold level set by the [SQL] control.

Pg 10-1 Main Unit
Block Diagram

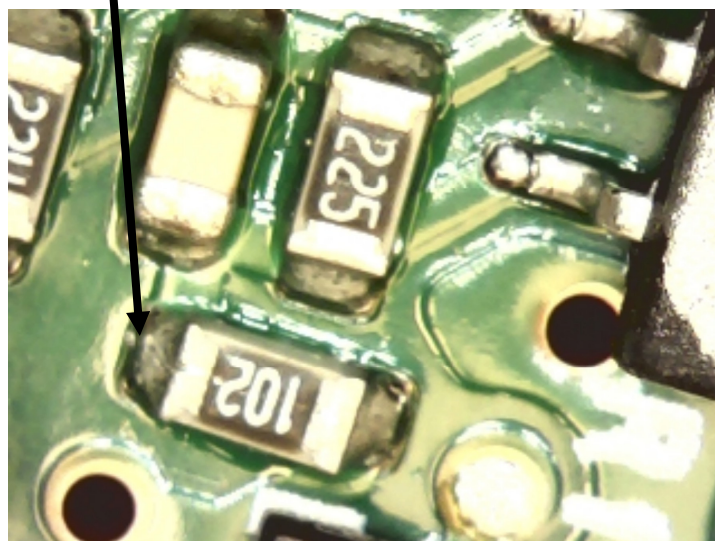


9-1 MAIN UNIT
• TOP VIEW



Pick point for SSI

Note R Number does not match
board layout



Used Digital Microscope to obtain photo